

**IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE**

APPLICANTS: Donald E. Steiss, et al.
APPLICATION NO.: 10/733,153
FILING DATE: December 10, 2003
TITLE: Thread Interleaving in a Multithreaded Embedded Processor
EXAMINER: William M. Treat
GROUP ART UNIT: 2181
ATTY. DKT. NO.: 22347-08260 (8116)

MAIL STOP AMENDMENT
COMMISSIONER OF PATENTS
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AMENDMENT A AND REPLY UNDER 37 CFR § 1.111

SIR:

In response to the non-final Office Action dated March 8, 2007, please amend the present application as follows and consider the included remarks below.